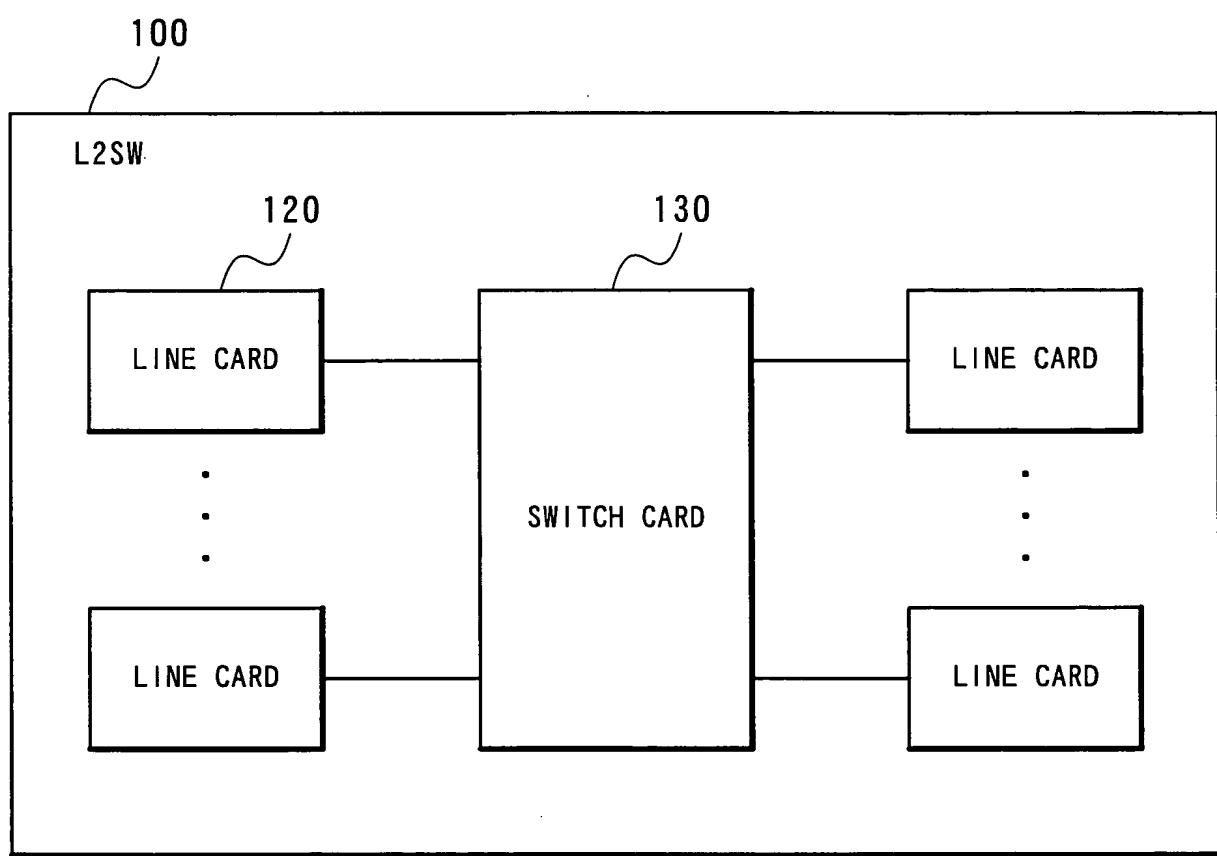
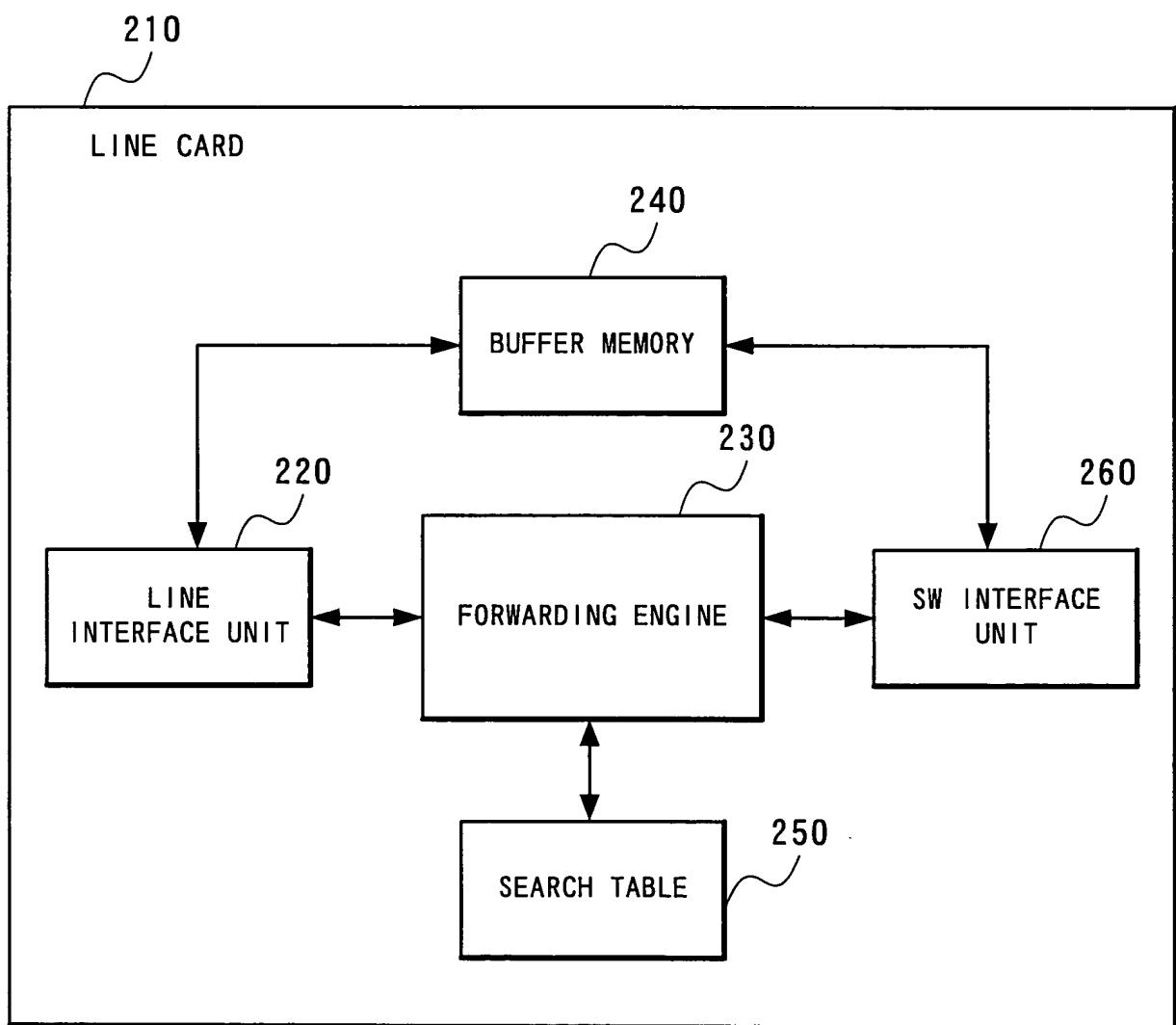
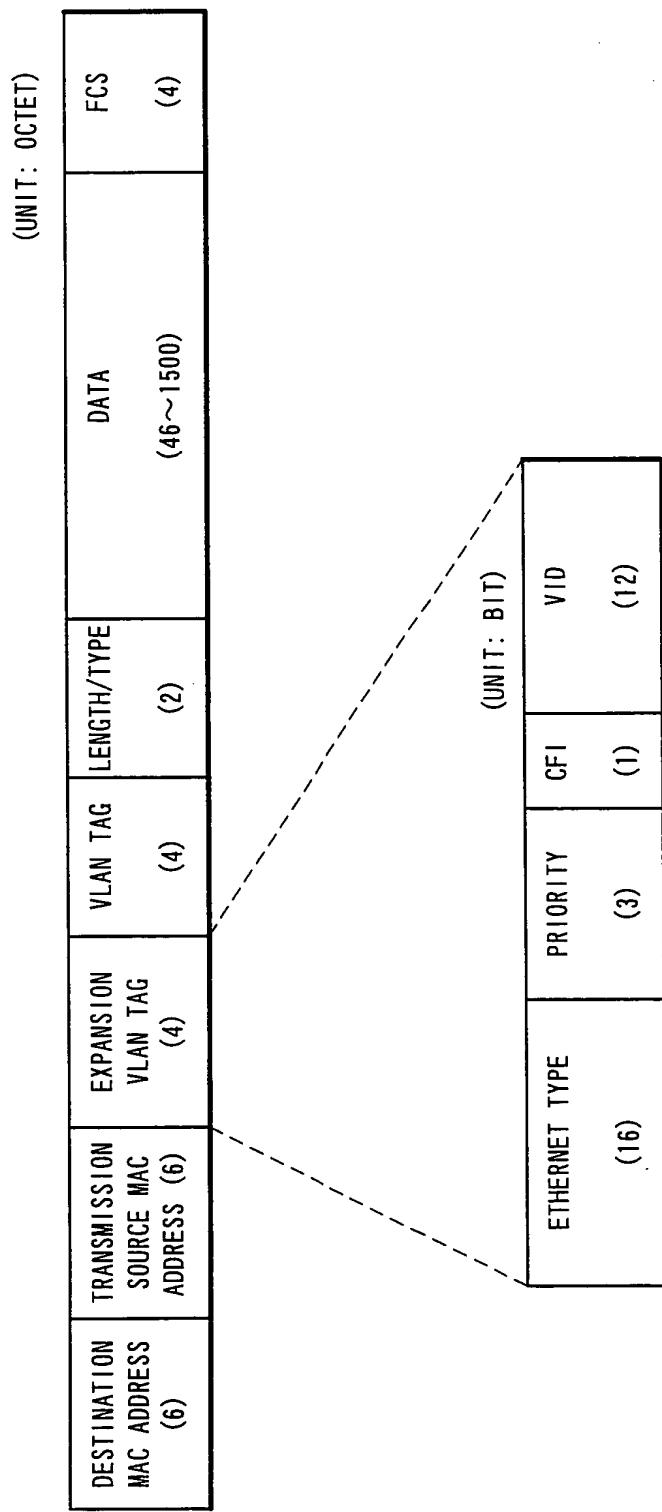
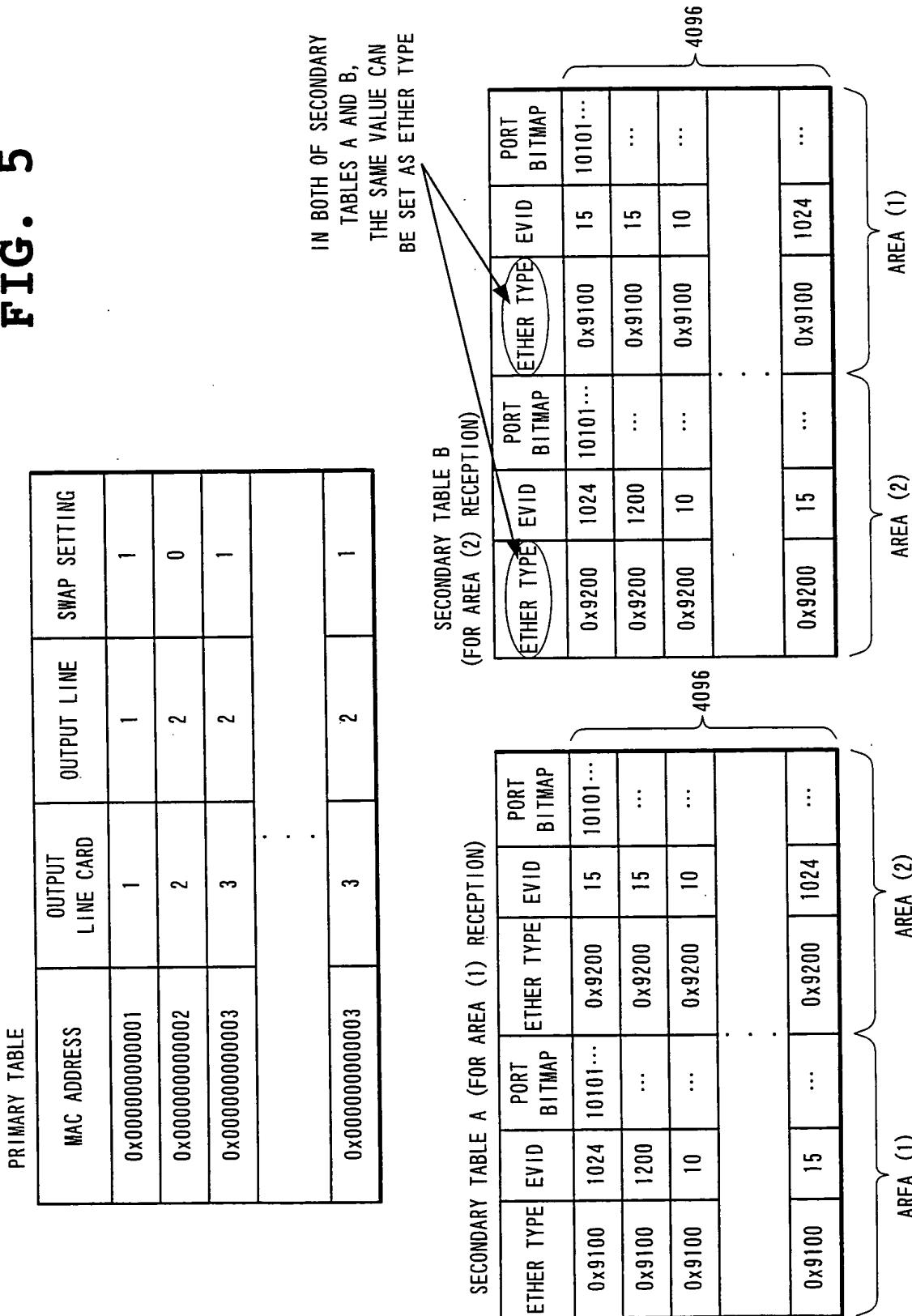


**FIG. 2**

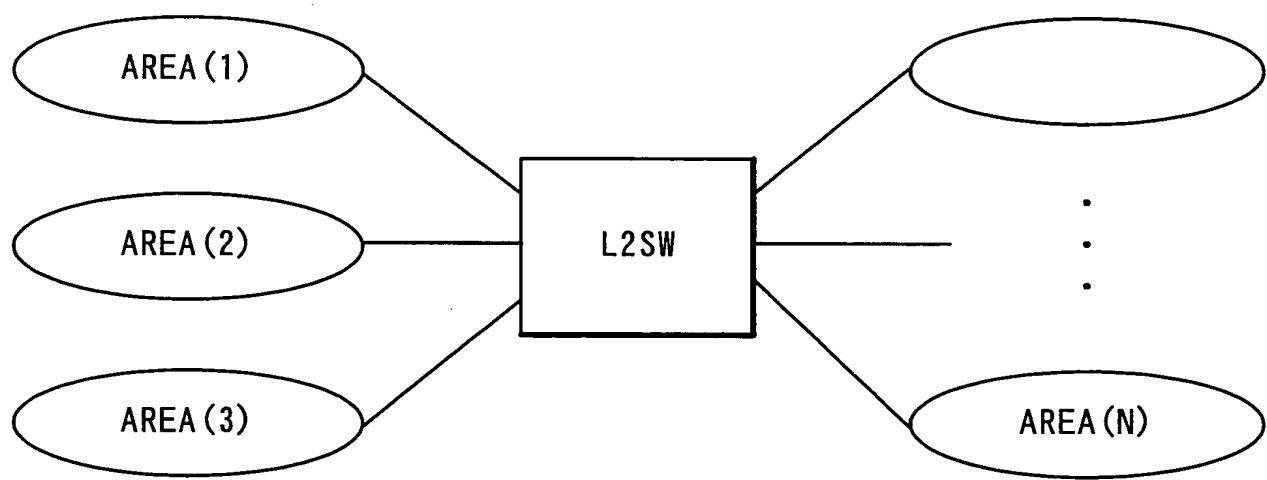
**FIG. 3**

**FIG. 4**



**FIG. 5**

**FIG. 6**



7 FIG.

**SECONDARY TABLE B  
(FOR RECEPTION OF OTHER AREA THAN AREA (1))**

The diagram illustrates the memory layout for two areas: **AREA (1)** and **OTHER AREA THAN AREA (1)**.

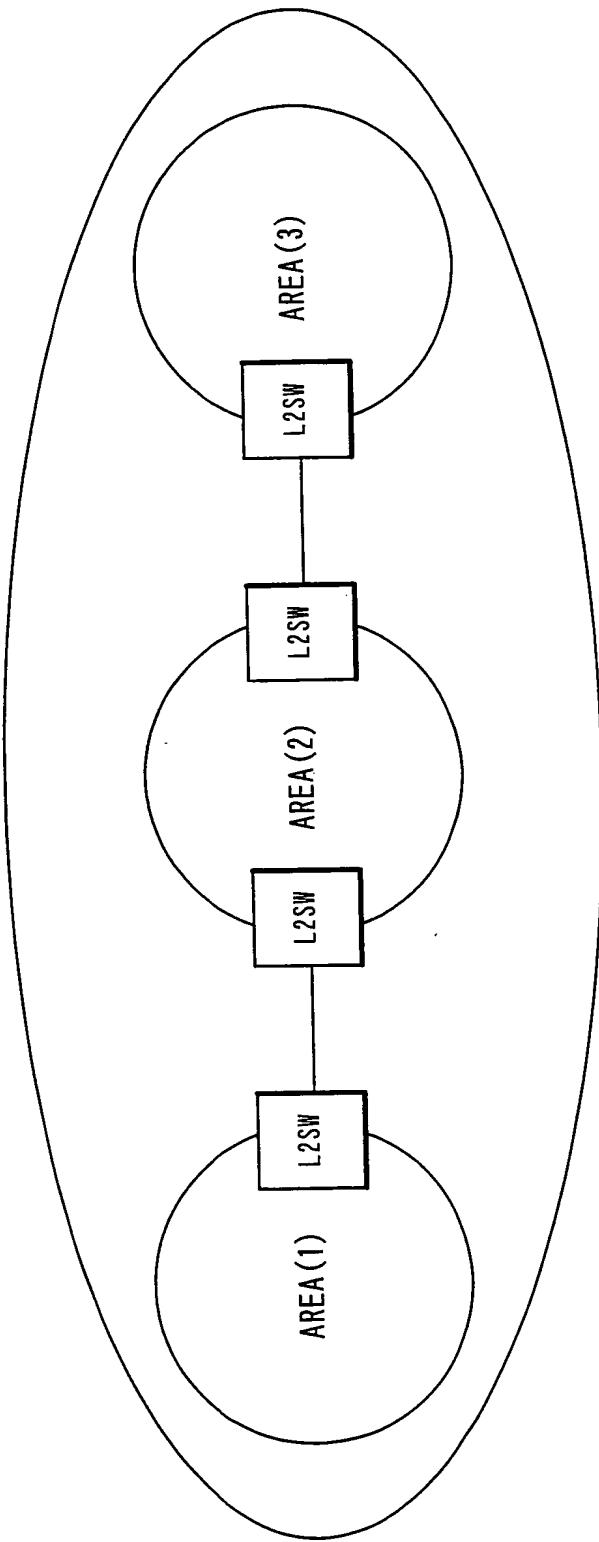
**AREA (1)** (Bottom Left):

- Contains **4096** bytes.
- Is divided into **(N-1)** blocks, each containing **4096** bytes.
- Each block contains **4096** entries, each entry having fields: **ETHER TYPE**, **EVID**, **PORT BITMAP**, **ETHER TYPE**, **EVID**, **PORT BITMAP**, **ETHER TYPE**, **EVID**, and **PORT BITMAP**.
- For the first entry (**Ether Type: 0x9100, Evid: 4000**), the values are: **0x9200**, **10101...**, **0x9200**, **10101...**, **0x9200**, **10101...**, **0x9200**, **10101...**, and **0x9200**.
- For the second entry (**Ether Type: 0x9100, Evid: 10**), the values are: **0x9300**, **15**, **...**, **0x9400**, **10**, **...**, **0x9999**, **1024**, **...**, and **0x9200**.

**OTHER AREA THAN AREA (1)** (Bottom Right):

- Contains **4096** bytes.
- Is divided into **(N-1)** blocks, each containing **4096** bytes.
- Each block contains **4096** entries, each entry having fields: **ETHER TYPE**, **EVID**, **PORT BITMAP**, **ETHER TYPE**, **EVID**, **PORT BITMAP**, **ETHER TYPE**, **EVID**, and **PORT BITMAP**.
- For the first entry (**Ether Type: 0x9100, Evid: 4000**), the values are: **0x9200**, **11**, **10101...**, **0x9300**, **16**, **...**, **0x9400**, **9**, **...**, and **0x9999**.
- For the second entry (**Ether Type: 0x9100, Evid: 10**), the values are: **0x9200**, **1200**, **...**, **0x9200**, **10**, **...**, **0x9200**, **15**, **...**, and **0x9200**.

**FIG. 8** (PRIOR ART)  
WIDE AREA ETHERNET NETWORK



**FIG. 9** (PRIOR ART)

